

(11) Publication number:

63003463 A

Generated Document.

PATENT ABSTRACTS OF JAPAN

(21) Application number: 61147381

(51) Intl. Cl.: H01L 29/78 H01L 21/205 H01L 27/12

(22) Application date: 24.06.86

(30) Priority:

(84) Designated contracting

publication:

(43) Date of application

08.01.88

(71) Applicant: AGENCY OF IND SCIENCE &

TECHNOL

SHIN ETSU CHEM CO LTD SEIKO INSTR & ELECTRONICS

(72) Inventor: HAYASHI YUTAKA

OKAZAKI SATOSHI **UMEMURA MITSUO** TAKADA RYOJI YAMANAKA MITSUYUKI

(74) Representative:

KAMIYA MASAAKI

THIN FILM TRANSISTOR (54) MANUFACTURE OF

(57) Abstract:

by thermal CVD of high-order silane mobility, by using a silicon film made operation characterized by high PURPOSE: To perform stable

05UU5405 A

such as trisilane or higher as a channel semiconductor film of a thin film transistor.

on the surface of the substrate by 4 is formed as follows: the substrate doublelayer structure of a P-or N-type silicon oxide film and silicon nitride evaporation, sputtering and the like substrate 1, a gate 2 comprising Ni, the substrate. thermal decomposition reaction on a chamber 7; and the film 4 is formed 400°C; the high order silane such as is heated to a temperature of about transistor is formed. The silicon film inverted staggered type thin film and a metal film, are formed. An low resistance semiconductor film thermal CVD method on the film 3. and the like on the gate 2. A silicon A gate insulating film 3 such as a W, Mo and the like is formed by CONSTITUTION: On an insulating the trisilane or higher is introduced in A source 5 and a drain 6, which have trisilane or higher is formed by a film 4 of high-order silane such as film is laminated by a CVD method

COPYRIGHT: (C)1988,JPO&Japio



